Fig. 1

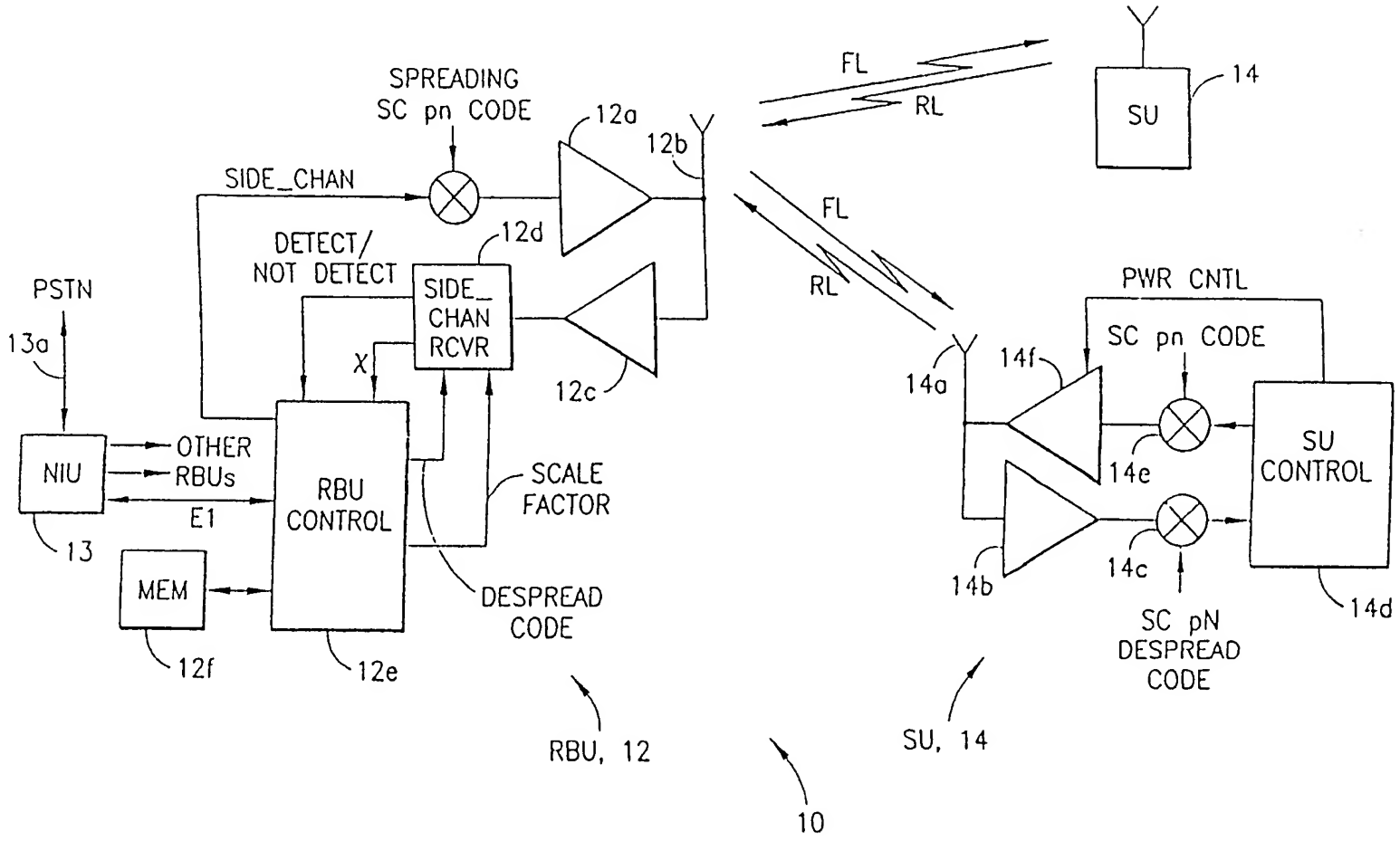

SPREADING
SC pn CODE
12a
12b
SIDE_CHAN
DETECT/
NOT DETECT
12d
SIDE_
CHAN
RCVR
X
12c
PSTN
13a
NIU
OTHER
RBUs
E1
13
RBU
CONTROL
SCALE
FACTOR
DESPREAD
CODE
MEM
12f
12e
RBU, 12
10
FL
RL
SU
14
FL
RL
PWR CNTL
SC pn CODE
14a
14f
14e
SU
CONTROL
14b
14c
14d
SC pN
DESPREAD
CODE
SU, 14

Fig. 2

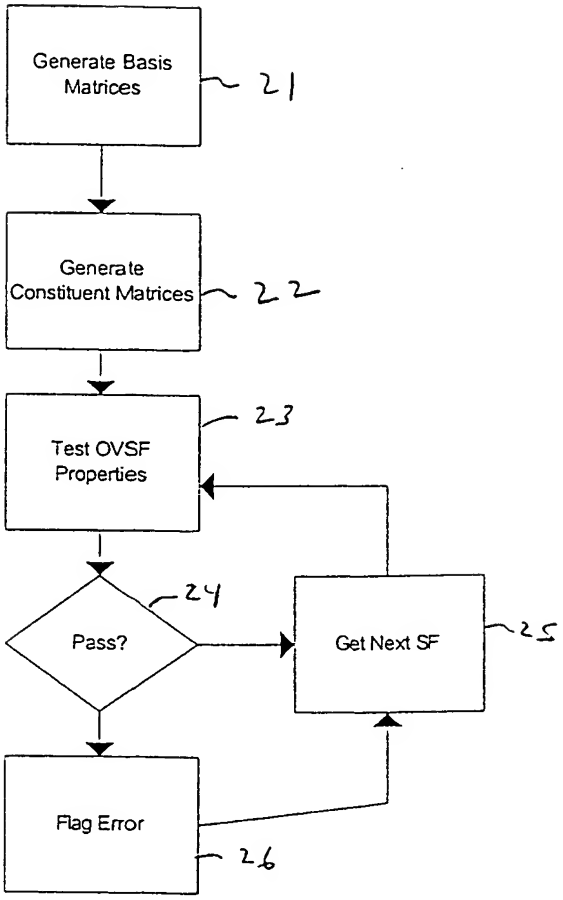

Generate Basis Matrices
21
Generate Constituent Matrices
22
Test OVSF Properties
23
Pass?
24
Get Next SF
25
Flag Error
26

Fig. 3

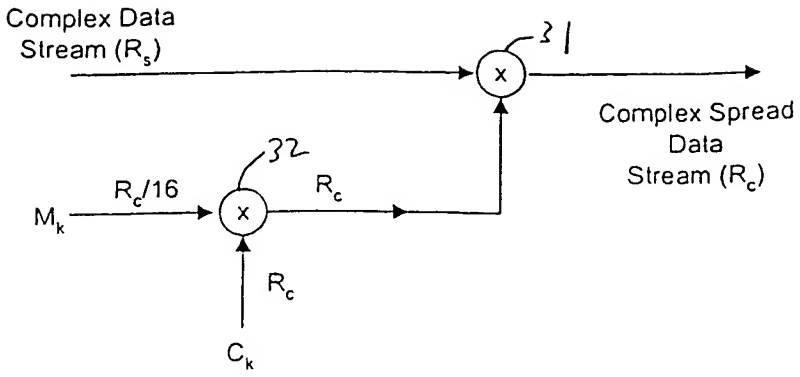


Fig. 4

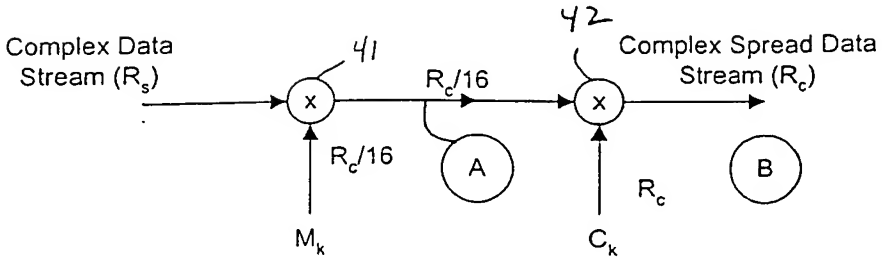


Fig. 5

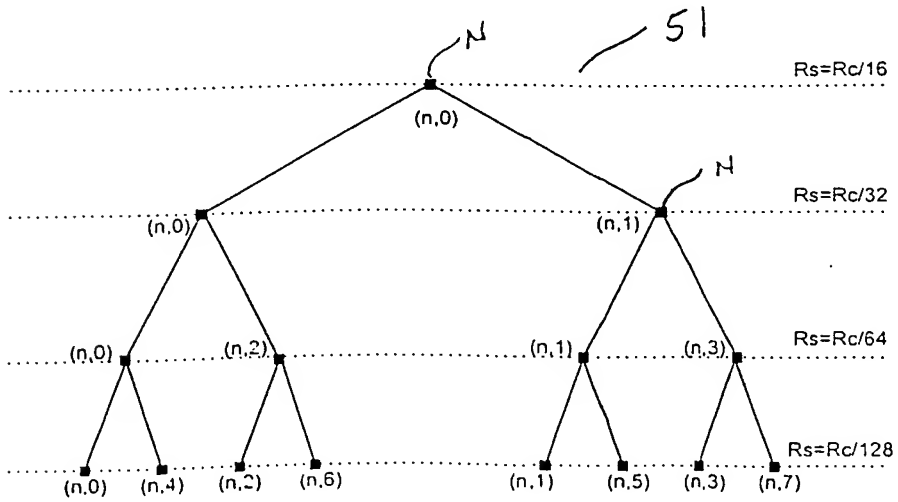
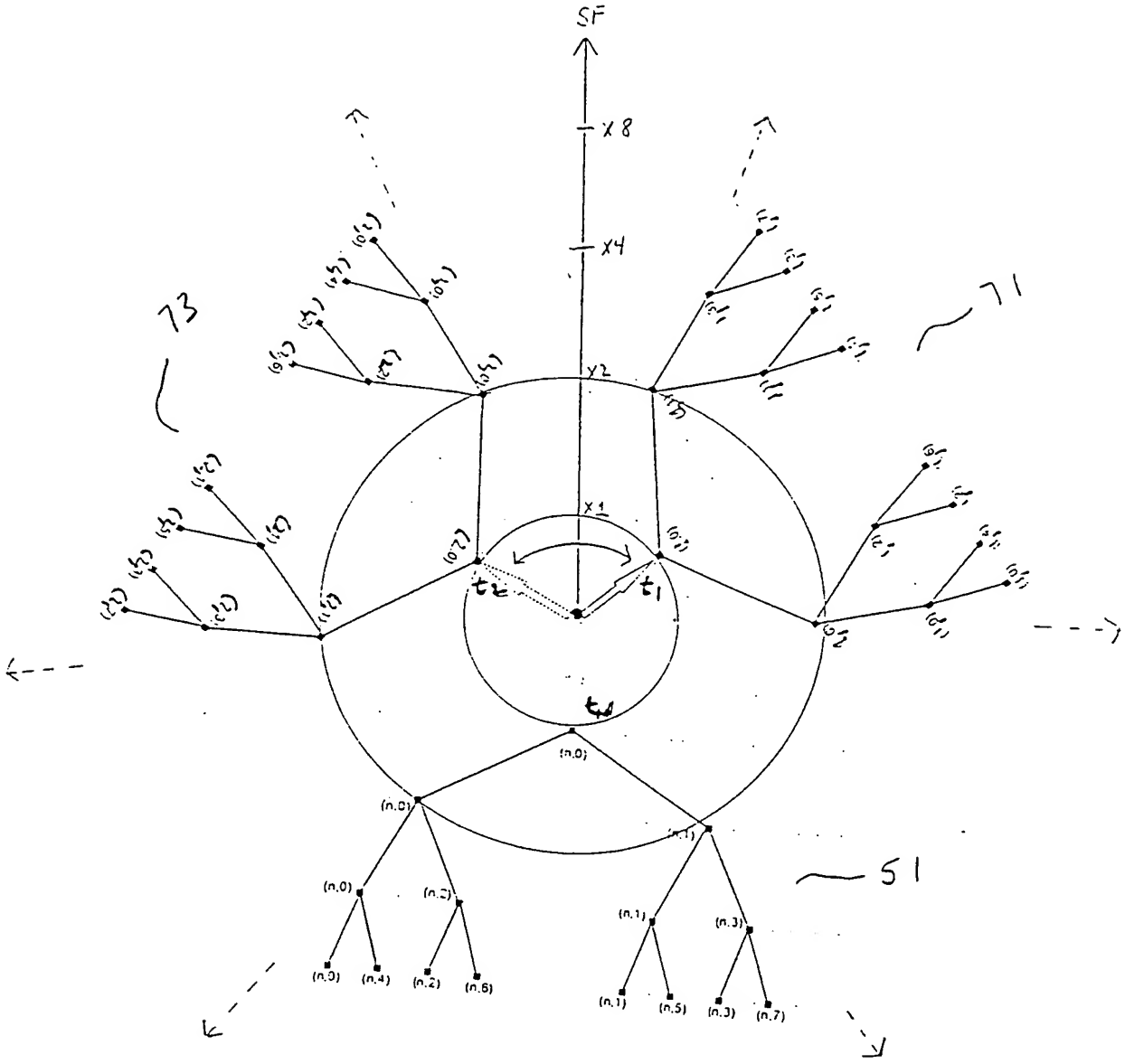


Fig. 6

Block nodes (n,k_b) for each active node (n,k_a) at the different symbol rates

Active Node (n,k_a)	Symbol Rate of Active Node (Rs)	Blocked Nodes (n,k_b) @ $R_s=R_c/16$	Blocked Nodes (n,k_b) @ $R_s=R_c/32$	Blocked Nodes (n,k_b) @ $R_s=R_c/64$	Blocked Nodes (n,k_b) @ $R_s=R_c/128$
(n,0)	$R_c/16$	NA	(n,0) and (n,1)	0,1,2,3	(n,k) w/ k=0,...,7
(n,0)	$R_c/32$	(n,0)	NA	0,2	(n,0), (n,2), (n,4) and (n,6)
(n,1)	$R_c/32$	(n,0)	NA	1,3	(n,1), (n,3), (n,5) and (n,7)
(n,0)	$R_c/64$	(n,0)	(n,0)	NA	(n,0) and (n,4)
(n,1)	$R_c/64$	(n,0)	(n,1)	NA	(n,1) and (n,5)
(n,2)	$R_c/64$	(n,0)	(n,0)	NA	(n,2) and (n,6)
(n,3)	$R_c/64$	(n,0)	(n,1)	NA	(n,3) and (n,7)
(n,0)	$R_c/128$	(n,0)	(n,0)	(n,0)	NA
(n,1)	$R_c/128$	(n,0)	(n,1)	(n,1)	NA
(n,2)	$R_c/128$	(n,0)	(n,0)	(n,2)	NA
(n,3)	$R_c/128$	(n,0)	(n,1)	(n,3)	NA
(n,4)	$R_c/128$	(n,0)	(n,0)	(n,0)	NA
(n,5)	$R_c/128$	(n,0)	(n,1)	(n,1)	NA
(n,6)	$R_c/128$	(n,0)	(n,0)	(n,2)	NA
(n,7)	$R_c/128$	(n,0)	(n,1)	(n,3)	NA

Fig. 7


SF
X8
X4
X2
X1
t2
t1
t4
(n,0)
(n,0)
(n,1)
(n,0)
(n,2)
(n,1)
(n,3)
(n,0)
(n,4)
(n,2)
(n,6)
(n,1)
(n,5)
(n,3)
(n,7)
71
73
51